

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

## Title of Invention

Zero-Insertion-Force Hinged Clam-Shell Socket for Testing  
Memory Modules

Application Number :

Confirmation Number:

First Named Applicant: Ramon Co

Attorney Docket Number: KT-16

Art Unit:

Examiner:

Search string: ( 6696848 or 6572396 or 6469496 or 6415397 or 6357023 or 6344751 or 6133745  
or 6071137 or 6046421 or 5973285 or 5813876 or 5795172 or 5461323 or 5153504  
or 4629270 or 4560221 or 4344033 or 4164704 ).pn

## US Patent Documents

**Note: Applicant is not required to submit a paper copy of cited US Patent Documents**

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6696848	2004-02-24	Robinson	B1	324	755
	2	6572396	2003-06-03	Rathburn	B1	439	260
	3	6469496	2002-10-22	Khouw , et al.	B1	324	158.1
	4	6415397	2002-07-02	Co , et al.	B1	714	42
	5	6357023	2002-03-12	Co , et al.	B1	714	42
	6	6344751	2002-02-05	Prokopp , et al.	B1	324	754
	7	6133745	2000-10-17	Yoon , et al.	B1	324	755
	8	6071137	2000-06-06	Rutigliano	B1	439	197
	9	6046421	2000-04-04	Ho	B1	209	573
	10	5973285	1999-10-26	Dietrich , et al.	B1	209	573
	11	5813876	1998-09-29	Rutigliano	B1	439	260
	12	5795172	1998-08-18	Shahriari , et al.	B1	439	260
	13	5461323	1995-10-24	Yanagi , et al.	B1	324	754
	14	5153504	1992-10-06	Bonnet , et al.	B1	324	754
	15	4629270	1986-12-16	Andrews, Jr. , et al.	B1	439	260
	16	4560221	1985-12-24	Olsson	B1	439	373
	17	4344033	1982-08-10	Stowers , et al.	B1	324	754
	18	4164704	1979-08-14	Kato , et al.	B1	324	72.5

**Signature**

Examiner Name	Date